

# **TOPCASED-DOORS Integration (TDI)**

**TOPCASED-DOORS** Integration (TDI)



#### **TDI Tool**



"RIF file".

DOORS

\_ 'SSDD' corrente 0.0 in /Prova2 (modulo Formale) - DOORS File Modifica Vista Inserisci Link Analisi Tabella Tool Discussioni Utente Change Management Guida F<sup>™</sup> 당<sup>™</sup> 년 - 1 문 공 1 |- 🏫 👎 \* = = ABC 8 ■+ ■+ ■> B I U abe 12 行 >> >> Vista Standard view ▼ Tutti i live ▼ 7 - C I 7 7 X 1 Q 88 2 0 A . . -SSDD ID 1 Scope: prova SSDD 1 Scope 2 Referenced documents 1 3 System-wide design de prova A DOORS Module (DOORS 4 System Functional Archi 1.1 Identification SSDD 5 Physical Architecture 2 9.3) can be exported as a 6 Functional to Physical N 7 Requirements traceabili SSDD 1.2 System overview 8 Notes 3 9 Intentionally left blank SSDD **1.3 Document overview** 10 Intentionally left blank It's format though is not 4 11 Intentionally left blank 12 Appendix 2 Referenced documents SSDD totally compliant with the 5 standard RIF12 format SSDD 3 System-wide design decisions 6 Viene fornita una descrizione di alto livello del sistema e delle modalità e SSDD 7 contesto di utilizzo. Non sono presenti requisiti. SSDD **4 System Functional Architecture** 8 In questo paragrafo deve essere descritta l'architettura funzionale del SSDD sistema. Questa deve dare evidenza di come le funzioni di sistema, 9 derivate dai requisiti di SSS di sistema, siano state decomposte in sottofunzioni realizzate da specifici componenti. A questo livello non deve 111 • ъ. 4 Nome utente: Administrator Modalità di modifica esclusiva





#### Import from DOORS

Convert → DOORS / RIF12

In this example SSDD.xml is the RIF exported from DOORS. It can be automatically converted in SSDD.rif12 (standard RIF format).







#### **EMF** generated **RIF** Editor

A RIF model can be opened and edited with the EMF generated RIF Editor, based on the RIF meta-model but...

Not very user friendly!

Specially for a DOORS user who is not a RIF expert: lots of information but very difficult to understand. DOORS structure and terminology not visible.



電

Document overview

LONGNAME

THEVALUE



### **TDI RIF Editor**

#### Based on the RIF meta-model, but:

- DOORS-like view: focused on DOORS models and objects
- New organization of attributes as properties or in customized editors
- DOORS levels: objects viewed in a parent/child hierarchy
- Improved creation/editing of links



#### SSDD.rif12

#### 🔁 Resource Set

- A B platform:/resource/DemoFDR/SSDD.rif12
  - ▲ ♦ RIF\_Document
    - DOORS\_Modules
      - <sPECGROUP> DOORS\_Formal\_Module SSDD
        - DOORS\_Object Scope
        - DOORS\_Object Referenced documents
        - DOORS\_Object System-wide design decisions
        - DOORS\_Object System Functional Architecture
          - Attributes
            - Object Text
          - DOORS\_Object Abs Num: 21
          - DOORS\_Object Abs Num: 59
          - DOORS\_Object Block Definition Diagram
          - DOORS\_Object Internal Block Diagram
          - ▲ ◆ DOORS\_Object Interface design
            - Attributes
            - DOORS\_Object Abs Num: 28
            - DOORS\_Object Abs Num: 41
            - DOORS\_Object DataTypes
          - DOORS\_Object System components

Selection Parent List Tree Table Tree with Columns

Properties	
Property	Value
ATTRIBUTEVALUEENUMERATION	
Created Thru	ENUMVALUE Extracting
IE Object Type	
ATTRIBUTEVALUESIMPLE	
Created On	·= 2011-06-17
Description	· 🖻
IE PUID	1
Last Modified On	<sup>1</sup> = 2012-01-12
Name	U.E.
Object Heading	System Functional Architecture



#### **TDI Synchronization**

#### DOORS RIF $\iff$ TOPCASED SysML

- Elements in TOPCASED SysML are in correspondence with elements in DOORS RIF
- SysML attributes are mapped onto DOORS attributes of corresponding elements in DOORS RIF and vice-versa
- Synchronization is an operation to guarantee that corresponding elements are aligned
- Synchronization is guided by a Synch Table that contains all the synchronization rules





Synch Table Concept



8

intecs

### Synch Table Model

EMF generated Editor based on a dedicated meta-model + some customization

Synch Table can be easily populated by hand to configure the needed synchronization or...



Ademo.synchtab 🖉 🐼	Bdemo.synchtab	sish DemoModel.sysmldi	DemoModel.sysm
🔁 Resource Set			
A 🗟 platform:/resource/DemoFDR/Ademo.synd	chtab		
Synch Table			
A 🔶 Element Row 1: Abs Num: 61 <-> R	equirement		
♦ Attribute Row IE PUID <-> Id			
Attribute Row Object Text <-> T	ext		
Element Row 2: Abs Num: 63 <-> R	equirement		
Element Row 3: Abs Num: 62 <-> R	equirement		
Element Row 4: Abs Num: 24 <-> R	equirement		
P latform:/resource/DemoFDR/DemoMode	el.sysml		
platform:/resource/DemoFDR/DemoReqPices/D	rofile.sysml		
▶ 🖗 platform:/resource/DemoFDR/SSDD.rif12			
pathmap://SysMLActivityExtensionsProfile.	uml		
A pathmap://SYSML_METAMODELS/SYSML.r	metamodel.uml		
» 💑 pathmap://UML_PROFILES/Ecore.profile.un	nl		
<ul> <li>» Seathmap://UML_PROFILES/Ecore.profile.ur</li> <li>» Seathmap://UML_LIBRARIES/UMLPrimitiveT</li> </ul>	nl Types.library.uml		
<ul> <li>&gt; &amp; pathmap://UML_PROFILES/Ecore.profile.ur</li> <li>&gt; &amp; pathmap://UML_LIBRARIES/UMLPrimitiveT</li> <li>&gt; &amp; pathmap://UML_METAMODELS/UML.meta</li> </ul>	nl Types.library.uml amodel.uml		
<ul> <li>&gt; athmap://UML_PROFILES/Ecore.profile.ur</li> <li>&gt; pathmap://UML_LIBRARIES/UMLPrimitiveT</li> <li>&gt; pathmap://UML_METAMODELS/UML.meta</li> <li>&gt; nathmap://IML_PROFILES/Standard.profile</li> </ul>	nl Types.library.uml amodel.uml e.uml		
<ul> <li>&gt; apathmap://UML_PROFILES/Ecore.profile.ur</li> <li>&gt; pathmap://UML_LIBRARIES/UMLPrimitiveT</li> <li>&gt; pathmap://UML_METAMODELS/UML.meta</li> <li>&gt; pathmap://UML_PROFILES/Standard profile</li> <li>Selection_Parent List_Tree_Table_Tree with Col</li> </ul>	nl Types.library.uml amodel.uml e.uml umns		
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_PROFILES/Standard profile     Selection_Parent_List_Tree_Table_Tree with Col     PropertiesProblems	nl Types.library.uml amodel.uml e.uml umns		e e
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://IML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems Property	nl Types.library.uml amodel.uml umns Walue		<b>1</b>
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://IML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems Property Create If Not In Rif	nl Types.library.uml amodel.uml umns Walue		<b>1</b>
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_PROFILES/Standard profile Selection Parent List Tree Table Tree with Col     Properties Problems Property Create If Not In Rif Create If Not In Sysml	nl Types.library.uml e.uml umns Value Walue State State Value		C E
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems Property Create If Not In Rif Create If Not In Sysml Direction	nl Types.library.uml e.uml umns Value % false % true Bidirec	tional	er e
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems     Property     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict	nl Types.library.uml amodel.uml e.uml umns Value % false % false % true Bidirec	tional	e E
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems     Property     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict     Remove In Rif If Removed In Sysml	nl ypes.library.uml amodel.uml e.uml umns Value <sup>105</sup> false <sup>105</sup> false <sup>105</sup> gidrec <sup>105</sup> Sysml <sup>106</sup> false	tional	E 1
A pathmap://UML_PROFILES/Ecore.profile.ur      A pathmap://UML_LIBRARIES/UMLPrimitiveT      A pathmap://UML_METAMODELS/UML.meta      A pathmap://UML_METAMODELS/UML.meta      A pathmap://UML_PROFILES/Standard profile      Selection Parent List Tree Table Tree with Col      Properties Problems      Property     Create If Not In Rif     Create If Not In Sysml     Direction      Master In Conflict      Remove In Rif If Removed In Sysml      Remove In Sysml If Removed In Rif	nl Types.library.uml amodel.uml e.uml umns Value % false % true % false % false % false	tional	<b>™</b> E
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://IML_PROFILES/Standard profile Selection Parent List Tree Table Tree with Col     Properties Problems Property Create If Not In Rif Create If Not In Sysml Direction Master In Conflict Remove In Rif If Removed In Sysml Remove In Sysml If Removed In Rif RI Fobject	nl ypes.library.uml amodel.uml e.uml umns Value Salue Sysml Sysm	tional 5_Object Abs Num: 61	2 E
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://IML_PROFILES/Standard profile Selection Parent List Tree Table Tree with Col     Properties Problems Property Create If Not In Rif Create If Not In Sysml Direction Master In Conflict Remove In Rif If Removed In Sysml Remove In Sysml If Removed In Rif RI Fobject RI Fparent	nl Types.library.uml amodel.uml e.uml umns Value Salue Sysml Sysml Sysml Sysml Sysml Sysml Sysml Sysml	tional 5_Object Abs Num: 61	<b>2</b> 4
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     D pathmap://UML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems     Property     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict     Remove In Rif If Removed In Sysml     Remove In Sysml If Removed In Rif     RI Fobject     RI Fparent     RI Fype	nl ypes.library.uml amodel.uml e.uml umns Value Safalse Sysml Sy	tional 5_Object Abs Num: 61 ement	E H
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     D pathmap://UML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems     Property     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict     Remove In Rif If Removed In Sysml     Remove In Sysml If Removed In Rif     RI Fobject     RI Fparent     RI Fype     Row Key	nl ypes.library.uml amodel.uml e.uml umns Value <sup>106</sup> false <sup>106</sup> false <sup>107</sup> false <sup>107</sup> false <sup>108</sup> false	tional 5_Object Abs Num: 61 ement	
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     D pathmap://UML_PROFILES/Standard profile     Selection Parent List Tree Table Tree with Col     Properties Problems     Property     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict     Remove In Rif If Removed In Sysml     Remove In Sysml If Removed In Rif     RI Fobject     RI Fparent     RI Ftype     Row Key     Sys MLobject	nl ypes.library.uml amodel.uml e.uml umns Value Salar False Sysml Sysml Sysml Sysml Sysml Require E Require E Require E Constant Sysml Sys	tional 5_Object Abs Num: 61 ement rfaceRequirement>> <requi< td=""><td>rement&gt; Requirement</td></requi<>	rement> Requirement
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_METAMODELS/UML.meta     Properties     Properties     Properties     Problems     Property     Create If Not In Rif     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict     Remove In Rif If Removed In Sysml     Remove In Sysml If Removed In Rif     RI Fobject     RI Fparent     RI Ftype     Row Key     Sys MLobject     Sys MLparent	nl ypes.library.uml amodel.uml e.uml umns Value % false % true % false % false % false % false % false % true % false % false % true % pooRs % false % false % false % false % false % false % false % pooRs % false % pooRs % false % pooRs % false % pooRs % false % pooRs %	tional 5_Object Abs Num: 61 ement rfaceRequirement>> <requi ige&gt; InterfaceRequirements</requi 	rement> Requirement
A pathmap://UML_PROFILES/Ecore.profile.ur     A pathmap://UML_LIBRARIES/UMLPrimitiveT     A pathmap://UML_METAMODELS/UML.meta     B pathmap://UML_METAMODELS/UML.meta     B pathmap://UML_METAMODELS/UML.meta     A pathmap://UML_METAMODELS/UML.meta     B pathmap://UML_METAMODELS/UML.Meta     Property     Create If Not In Rif     Create If Not In Rif     Create If Not In Sysml     Direction     Master In Conflict     Remove In Rif If Removed In Sysml     Remove In Sysml If Removed In Rif     RI Fobject     RI Foprent     RI Ftype     Row Key     Sys MLobject     Sys MLobject     Sys MLstereotype	nl ypes.library.uml amodel.uml e.uml umns Value % false % true % false % false % false % false % false % true % DOOR! % false % true % DOOR! % false % true % false % fa	tional 5_Object Abs Num: 61 ement rfaceRequirement>> <requi ige&gt; InterfaceRequirements otype&gt; InterfaceRequirements</requi 	rement> Requirement



### Synch Table Wizards [1/4]

DOORS

DOORS

Formal

Modules

**TOPCASED-DOORS** Integration (TDI)

- A: RIF Requirement ------> SysML Requirement

Wizards: Simple configuration input to automatically add rows to a Synch Table for classical synchronization scenarios

TDI

RIF

DSL

Models

EDITOR -

**ECLIPSE** 

TDI

Tool

TOPCASED

**SysML** 

Models

and

Diagrams





A: RIF Requirement

## Synch Table Wizards [2/4]

#### → SysML Requirement

Wizard A example: all requirements in under RIF parent "System Functional Architecture" must have a correlated Requirement in TOPCASED under SysML parent "Interface Requirements". These requirements in SysML shall be stereotyped as DemoReqProfile::InterfaceRequirement. Attributes:

- RIF Object Text = SysML Text
- RIF IE PUID = SysML Id .

RIF Parent	System Functional Architecture
SysML Parent	InterfaceRequirements 🔹
Correspondence	Object Text=>Text IE PUID=>Id
Stereotype	DemoReqProfile::InterfaceRequirement
OK Cancel	





intecs Brainware company

## Synch Table Wizards [3/4]

#### B: SysML Requirement

Wizard B example: all requirements stereotyped as DemoReqProfile::UserRequirement in TOPCASED under SysML parent "Demo Model" must have a correlated Requirement under RIF parent "Added by TDI User Requirements". Attributes:

- RIF Object Text = SysML Text
- RIF IE PUID = SysML Id .

RIF Requirement	
-----------------	--

	Wizard Scenario B:	Requirements SysML	-> RIF		
	SysML Parent RIF Parent Stereotype		DemoModel   Added by TDI User requirements  DemoReqProfile::UserRequirement		
(	Correspondence		Text=>0 Id=> <mark>I</mark> E	Dbject Text PUID	
2	ОК	Cancel			
Create if not exists in Rif	Create if not exists in SysML	Remove from Sysl deleted in Rif	ML if	Remove from Rif if deleted in SysML	Master in case of conflict
X				X	SysML
DOORS DOORS Formal					



### Synch Table Wizards [4/4]

Wizards:

- Automatically add rows to a Synch Table for classical synchronization scenarios
- Consistency checks and automatic deletion of obsolete rows (with confirmation from user)



o.synchtab - Eclipse Platform	Survey of States
cripts SmartQVT Run Synchtab Editor Window	Help
⋪ ▾ │∞ │● │ <b>●</b> │	
🛿 Ademo.synchtab 🖾 🙀 DemoModel.sysml 📄 🙀	🕯 DemoModel.sysmldi 📄 🚱 DemoModel.sysml 📄 🐼 ADemo.s
Resource Set	
<ul> <li>Platform:/resource/DemoFDR/Ademo.synchtable</li> <li>Synch Table</li> <li>Element Row 1: Abs Num: 61 &lt;-&gt; Require</li> <li>Attribute Row IE PUID &lt;-&gt; Id</li> <li>Attribute Row Object Text &lt;-&gt; Text</li> <li>Element Row 2: Abs Num: 63 &lt;-&gt; Require</li> <li>Element Row 3: Abs Num: 62 &lt;-&gt; Require</li> <li>Element Row 4: Abs Num: 24 &lt;-&gt; Require</li> <li>platform:/resource/DemoFDR/DemoModel.syster</li> <li>platform:/resource/DemoFDR/DemoReqProfile</li> <li>platform:/resource/DemoFDR/DemoReqProfile</li> <li>Parent List Tree Table Tree with Columns</li> </ul>	rement rement rement ml sysml
Properties Problems	
	Value
Create If Not In Rif	₩ false
Create If Not In Sysml	<sup>EX</sup> true
Direction	<sup>™</sup> Bidirectional
Master In Conflict	<sup>i</sup> Sysml
Remove In Rif If Removed In Sysml	🔤 false
Remove In SysmI If Removed In Rif	<sup>™</sup> true
RI Fobject	DOORS_Object Abs Num: 61
RI Fparent	
RI Ftype	Requirement
Row Key	<sup>1</sup> 1
Sys MLobject	<interfacerequirement>&gt; <requirement> Requirement</requirement></interfacerequirement>
Sys MLparent	<package> InterfaceRequirements</package>
Sys MLstereotype	Stereotype> InterfaceRequirement
Sys MLtype	<sup>™</sup> Requirement

### **TDI Synchronization**

- Triggered from Synch Table's contextual menu: «Synchronize»
- Based on a Synch Table: fine grain configurability
- SysML and RIF12 elements are aligned according to Synch Table's content
- Synch Table is updated upon synchronization so synch connections between RIF and SysML are maintained



Sit *DemoModel.sy	smldi 🛿		- 6
DemoFDR/DemoN	Aodel.sysmldi		
Select	req [package] InterfaceRequirements [Inte	erfaceRequirements]	*
• 江 Marquee	< <requirement interfacer<="" td=""><td></td><td></td></requirement>		
Note	Requirement 001		E
≥ Objects 🛛 ∞	Text : "è una FlowSpecification		
Package	Ouesto deve essere descritto in		
Requirement	termine di valore di RF (Hz) ed	< <requirement, interfacerequirement<="" td=""><td></td></requirement,>	
THEM ACA	ampiezza (amplitude) espressa	Requirement 003	
Connections &	in dBm	lext : "e un tipo di dato rappresentante	
🖧 Conform		in termini intervallo di frequenza (RFMin,	
Containment	Id : "SSDD.INT.0001"	RFMax) espresso in Hz e sensbilità espressa	Ī
A.Course		în dBm	
🔁 Comments 👳		w	
🕨 🖻 Comment		Id : "SSDD.INT.0003"	
🖻 Constraint		<u>.</u>	
Consment	<	III	Þ.
🗏 Properties 🔡 Pr	oblems 🗄 Outline		₩ 2 8 7 8 8
3			
🗳 <model> De</model>	emoModel		<b>^</b>
🖺 <package< td=""><td>&gt; DemoModel</td><td></td><td></td></package<>	> DemoModel		
🖻 <packa< td=""><td>age&gt; UserRequirements</td><td></td><td></td></packa<>	age> UserRequirements		
💾 <packa< td=""><td>age&gt; SystemRequirements</td><td></td><td></td></packa<>	age> SystemRequirements		
📫 <pa< td=""><td>ckage&gt; InterfaceRequirements</td><td></td><td></td></pa<>	ckage> InterfaceRequirements		
EE0		ent> Requirement 004	
> <u></u>	<interfacerequirement>&gt; <requirement< td=""><td>ent&gt; Requirement 002</td><td></td></requirement<></interfacerequirement>	ent> Requirement 002	
>	<interfacerequirement>&gt; <requirement< td=""><td>ent&gt; Requirement 001</td><td></td></requirement<></interfacerequirement>	ent> Requirement 001	
	conterfaceRequirement>> < Requirement	ent> Requirement 005	
	vstemBequirement>> < Requirement>	SystemRequirement1	
<u></u> = <<3 <u></u> = <<3	vstemRequirement>> <requirement></requirement>	SystemRequirement?	
C Reg	uirement diagram SystemRequireme	ents	
Slock	System		*
search			case sensitive



#### Export to DOORS

D ..

#### Convert RIF12 $\longrightarrow$ DOORS

After carrying out synchronization between RIF and TOPCASED SysML, the edited RIF12 file can be converted to DOORS format (SSDD.xml) and exported back into DOORS.



Vavigator 🛛 🧶 Tope	cased Navigator 📃 🗆	si DemoModel.sysmldi 🛛 🚱 SSDD.rif12 🛛
	(> -> @   🖻 🕏 📚 ▽	Part Resource Set
<ul> <li>DemoFDR</li> <li>.project</li> <li>Ademo.synchta</li> <li>Bdemo.synchta</li> <li>DemoModel.sy</li> <li>DemoModel.sy</li> <li>DemoReqProf</li> <li>SCDD st12</li> </ul>	ab ab ysml ysmldi ile.sysml	<ul> <li>DOORS_Modules</li> <li><specgroup> DOORS_Formal_Modules</specgroup></li> <li>DOORS_Object Scope</li> <li>DOORS_Object Referenced docum</li> <li>DOORS_Object System-wide desig</li> <li>DOORS_Object System Functional a</li> <li>DOORS_Object Added by TDI User</li> </ul>
SSDD.rif12   SSDD.xml   External Files   ProfiliElt   Prove   TCFuffa   Test   Test001   Copy   Paste   Delete   Move   Rename   Prova   .project   aaa.syncht		
<ul> <li>anotherTa</li> <li>bisWizA.sy</li> <li>createdFrc</li> <li>figura.rif12</li> <li>FuffaProfil</li> <li>FuffaProfil</li> <li>handMade</li> <li>My.rif12</li> <li>My.workt</li> </ul>	Refresh Run As Debug As Profile As Team Compare With Replace With Topcased	<pre>hified h h h h h h h h h h h h h h h h h h h</pre>
Image: Synchronic and Synchroic and Synchronic and Synchronic and Synchronic and Synchronic an		Convert to Doors/RIF 1.2
<ul> <li>oliver.rif12</li> <li>Prova.sysn</li> <li>Prova.sysn</li> </ul>	Properties Open As	•
provavene	Remove from Context Ctrl+Alt+ Mark as Landmark Ctrl+A	-Shift+Down III Alt+Shift+Up

D'

intecs Brainware company

#### ... back in DOORS

RIF file imported back into DOORS. All changes are merged into original DOORS Module







#### **TDI Tool Flexibility**

Relying on the RIF standard format, a high level of flexibility is guaranteed:

• *despite its name, TDI could work with any requirement management tool that supports RIF, other than DOORS* 

